UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,029,285 B2 Page 1 of 1

APPLICATION NO.: 10/809846
DATED: April 18, 2006
INVENTOR(S): Takashi Abe

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 41, after "in the memory bus", insert -- 110 D --

Signed and Sealed this

Twentieth Day of October, 2009

David J. Kappas

David J. Kappos

Director of the United States Patent and Trademark Office